Jawaharlal Nehru Technological University, the **First Technological University** of India, was established on 2nd October 1972 in Andhra Pradesh with head quarters located in a historical city **Hyderabad.** The University is one of the premier Universities in India accredited by **NAAC** with **'A'** Grade. After successful and proven levels of appreciated existence and stature spanning over 36 years, JNTU has been divided into four different universities by Govt. of Andhra Pradesh, through Act No.30, Dt. 24th September, 2008. One of the constituent colleges of the University **"JNTUH College of Engineering, Hyderabad"** is regarded as a pioneer in shaping the excellence of some of the leading organizations of the industry, by churning out the finest professionals with a resolve to scale greater heights in the technological scenario, every year.

The Department of **Electronics and Communication Engineering,** established in 1973, is instrumental in molding the careers of students and helping them to become world-class professionals. Besides highly qualified and experienced staff and well-equipped laboratories, the Department has been awarded **8.1** points out of **10** by the **State Board of Technical Education.**

About Cranes software international Ltd.,

Cranes Software International Limited provides Enterprise statistical analytics and Engineering Simulation software, software Products and solutions for clients across the globe. The company's business interests span Products, Productized solutions, Services and R&D in future technologies. Also Cranes Software International Limited is the Sole Authorized Partner for Texas Instruments India University Program to set up Laboratories with MSP430, DSP (Digital Signal Processing) and Analog Development Tools. Cranes Software has 15 years of experience in Scientific & Engineering Product Sales & Services which has resulted in 1500+ Engineering Colleges as customers across India.

About Workshop:

TMS320C6748/OMAP L138 is a high performance 32bit floating point processor from Texas Instruments. TMS320C6748/OMAP L138 DSP'S find widespread use in applications like embedded analytics and real-time signal processing, including biometric analytics, communications and audio. In this workshop, the intention is to implement the concepts of digital signal processing into the Digital Signal Processor. The TMS320C6748/OMAP L138 UNIV KIT along with code composer IDE will be used to illustrate these concepts. The University kit will have DSP TMS320C6748/OMAP L138 SOM along with AIC3104 CODEC for doing real time audio and signal processing applications and also the board includes led and switches which are user programmable. The workshop will include lectures, demonstrations, and hands-on experiments. The participants will be permitted to carry out hands-on experiments on the DSP board

Topics to be covered:

Introduction to TI DSP family
Introduction to the OMAP-L138 experimenter /LCDK and Code Composer Studio
Analogue input and output using the OMAP-L138 Introduction to FIR filtering
Introduction to IIR filtering
The fast Fourier transform, Adaptive filtering

Introduction to SYS/BIOS
Each lab session will comprise a short introductory presentation and demonstration and then participants will follow instructions from a lab sheet under guidance from the instructor

About Resource person:

Dr. Donald Reay is a Lecturer at Heriot-Watt University in Edinburgh, Scotland. He has also taught hands-on DSP, on a number of occasions, as a visiting lecturer at Zhejiang University in Hangzhou, China. He coauthored, with Rulph Chassaing, the Second Edition of Digital Signal Processing and Applications with the TMS320C6713 and TMS320C6416 DSK, also published by Wiley. He is the author of the book "Digital Signal Processing and Applications with the OMAP L-138 eXperimenter" published by Wiley. Previously he has worked on speech recognition at British Telecom Research Laboratories and, before that, on industrial robotics at EMI Electronics. H e has published over 50 journal and conference paapers.

Eligibility:

Faculty of Educational Institutions/ Graduate Engineers/ Research Scholars/ Research Personnel working in R& D Organization, with a minimum qualification of Bachelor's degree in Electronics & Communication/ Electrical/ Computer Science and allied branches of Engineering & PG students are eligible to apply.

Important Dates:

Last date for registration: **22-06-2013**
*Time: **Workshop begins sharply by 10.00 AM on 24-06-2013***

How to Apply:

Interested students may apply in the prescribed format and the application be **strictly** sent on or before **22-06-2013 to** workshoptiepc@gmail.com As the number of participants is limited to only 50 the participants are advised to register well in advance of the scheduled dates.

Registration Fee:

Rs 500/- for Student
Rs 1000/- for Faculty
Rs 2000/- for Person from Industry

Accommodation:

Limited accommodation is available for the participants in the Guest House on first come first serve basis on payment basis for an amount of Rs.600/-(Towards boarding & Lodging Charges) per day. However working lunch and snacks will be provided for all the participants.

Patron:
Prof. Rameshswar Rao,
Vice Chancellor, JNTUH
Co-Patron:
Dr. A. Vinaya Babu,
Principal, JNTUH CEH
Head of ECE
Dr.D. Srinivasa Rao
Faculty
Dr. P. V. D. Soma Sekhar Rao
Dr. L. Pratap Reddy
Dr. M. Madhavi Latha
Dr. M. Asha Rani
Dr. B. N. Bhandari
Smt. N. Mangala Gouri
Dr. T. Satya Savitri
Dr. K. Anitha Sheela
Mrs. T. Madhavi Kumari
Sri. C. Bharatendra Rao
Mrs. P. Soujanya Sridevi

Address for Correspondence

Dr.M.Asha Rani, Dr.K.Anitha Sheela
Department of ECE, JNTUHCEH, JHNUH, Kukatpally, Hyderabad.
Cell: 9441189219, 9848564860
E-mail: workshoptiepc@gamail.com

Registration Form

Name of the Applicant:…………………..
(Block Letters)
…………………………….………..

Designation:…………………………….…

Gender: (M/F): …..

Educational Qualifications:……………

Address for Correspondence :(Including E-mail, Fax, Cell / Landline)
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Name of the Department & Specialization:
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Signature of the Applicant with date

**Two days
Workshop on
"REAL TIME DIGITAL SIGNAL
PROCESSING & APPLICATIONS
WITH OMAP-L138"**
(Under TEQIP)
24th & 25th June, 2013

Venue
TI Embedded Processing Centre
ECE Department, JNTUHCEH

Organized by

Department of E. C. E
JNTUH College of Engineering
Hyderabad
Coordinators
Dr.M.Asha Rani, Professor of ECE,JNTUHCEH
Dr.K.Anitha Sheela, Assoc. Professor of ECE,JNTUHCEH

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